



# Binary level code analysis : Side channel, Fault injection & Library stubs

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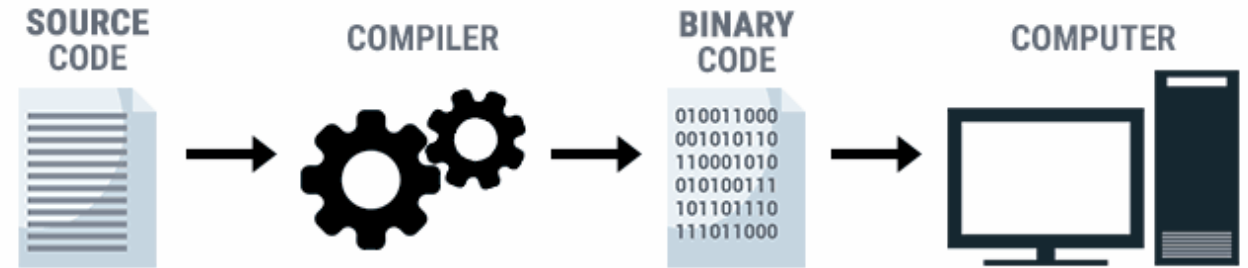


# A need for binary level analysis



COTS

**No source code**



**Malware**

**What You See  
Is Not What You Execute**



# BINSEC in a nutshell (since 2012)



binary lifting,  
IR, CFG, call graph,  
symbolic execution,  
static analysis, ..

**Vulnerability  
Assessment**

Security critical components

- Fault injection
- Side channel attack
- Attacker model

Symbolic engine

**BINSEC**

Generic IR

Solvers

Decoders

Supply chain

**Bug finding**

- Advanced fuzzing
- Test case generation

Malware comprehension

**Reverse  
Engineering**

- Capture The Flag
- Deobuscation
- Decompilation



arm



PowerPC



OCaml  
56k LOC

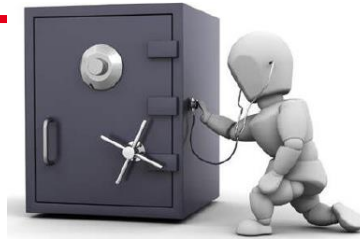


UNISIM  
Virtual  
Platforms  
.org

# Research project topics

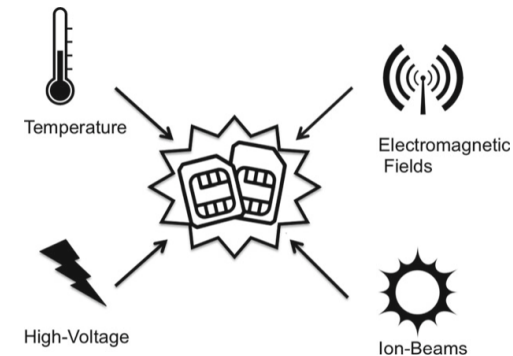
## ■ Side Channel Attacks

- **Leakages.** Timing information, power consumption, electromagnetic leaks and sound, etc.
- **Constant time verification.** IEEE S&P 2020, NDSS 2021, CCS 2023 ✓
- **Goal.** Handling of new threat models (e.g. Power attacks, Ciphertext attacks)

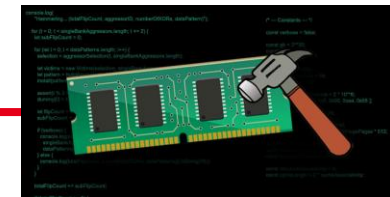


## ■ Fault Injection Attacks

- **Perturbations.** High voltage, extreme temperature, electromagnetic pulses, laser beam, etc.



- **Adversarial symbolic execution.** ESOP 2023 ✓
- **Goal.** Handling of new threat models (e.g. RowHammer) or improvement of the analysis scalability



## ■ Library stubs



- **Missing code.** Dynamically linked library (e.g. libc), syscalls, etc.
- **High-level concepts.** File system, string, etc.
- **Goal.** Improvement of the expressiveness, the genericity or the automation of the stubbing mechanism